

Sub B1 Encl

[Please amend claim 2 as follows:]

2. (Amended) A piezoelectric oscillator, wherein, in an inverter piezoelectric oscillator in which a piezoelectric resonator is connected between an input terminal and an output terminal of an inverter amplifier; and divisional capacitors C1 and C2 are connected between respective ends of the piezoelectric resonator and the ground, and wherein by inserting a MOS construction type capacitance element in series with the piezoelectric resonator, one end of the MOS construction type capacitance element is applied with a bias voltage which is the V voltage at an output end or input end of the inverter amplifier and the other end of the MOS construction type capacitance element is supplied with a control voltage that varies within a range whose intermediate value is the V voltage.

A19 (contd)

[Please amend claim 3 as follows:]

3. (Amended) A piezoelectric oscillator, wherein, in an inverter piezoelectric oscillator in which a piezoelectric resonator is connected between an input terminal and an output terminal of an inverter amplifier; and divisional capacitors C1 and C2 are connected between respective ends of the piezoelectric resonator and the ground, and wherein two MOS construction type capacitance elements are inserted respectively on both sides of the piezoelectric resonator; one end of each of the MOS construction type capacitance elements is applied with an alternating current voltage whose intermediate voltage is a V voltage; and the other end of the MOS construction type capacitance element is applied with a control voltage that varies within a range whose intermediate value is the V voltage.

[Please amend claim 4 as follows:]

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4. (Amended) A piezoelectric oscillator, wherein, in an inverter oscillator in which a piezoelectric element is connected to an input or output end of an inverter amplifier; and divisional capacitors C1 and C2 are connected between respective ends of the piezoelectric element and the ground, and wherein a MOS construction type capacitance element is inserted between the piezoelectric resonator and an input end of the inverter amplifier or between the piezoelectric resonator and an output end of the inverter amplifier; a control voltage V_{cont} is applied to the terminal on a connection-to-piezoelectric resonator side of the MOS construction type capacitance element; and, when it is assumed that V represents the voltage that is a direct

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Amended*

current bias voltage at the input end or output end of the inverter amplifier and that is applied to one end of the MOS construction type capacitance element, it is arranged that said voltage becomes an intermediate voltage of the control voltage V_{cont} .

Please amend claim 5 as follows:

*A19
(cont'd)*

5. (Amended) A piezoelectric oscillator, wherein, in an inverter oscillator in which a piezoelectric element is connected to an input or output end of an inverter amplifier; and divisional capacitors C1 and C2 are connected between respective ends of the piezoelectric element and the ground, and wherein a MOS construction type capacitance element is inserted between the piezoelectric resonator and an input end of the inverter amplifier or between the piezoelectric resonator and an output end of the inverter amplifier; a control voltage V_{cont} is applied to the terminal on the connection-to-piezoelectric resonator side of the MOS construction type capacitance element; and a direct current circuit of a resistor and a capacitor is inserted and connected between the terminal on the inverter-amplifier side of the MOS construction type capacitance element and the input or output terminal of the inverter amplifier; and further a direct current bias voltage is applied to the terminal on the inverter-amplifier side of the MOS construction type capacitance element.

Please amend claim 6 as follows:

6. (Amended) A piezoelectric oscillator according to claim 5, wherein the amplitude level of an alternating current supplied to the MOS construction type capacitance element is adjusted according to the value of the resistance of the direct current circuit; and when it is assumed that V represents the direct current bias voltage supplied to the terminal on the inverter-amplifier side of the MOS construction type capacitance element, it is arranged that the direct current bias voltage V becomes an intermediate voltage of the control voltage V_{cont} .

REMARKS

Applicant has amended claims 1-6, specification, abstract and the drawings. Applicant respectfully submits that the amendments to the claims, specification, abstract and drawings are supported by the application as originally filed and do not contain any new matter. Accordingly, the Office Action will be discussed in terms of the claims, specification, abstract and drawings as amended.